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INDICATION FORM**

Application Number	10/580,361
Filing Date	04-18-2007
First Named Inventor	Roy Knechtel
Title	Protection of Semiconductor Substrates With Buried Layers by Jittery (Buried) Semiconductor Wires
Art Unit	2811
Examiner Name	
Attorney Docket Number	LEO 003 PA

I hereby revoke all previous powers of attorney given in the above-identified application.

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29673

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Name	Registration Number

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I am the:

☐ Applicant/inventor.

☒ Assignee of record of the entire interest. See 37 CFR 3.71.  
Statement under 37 CFR 3.73(b) is enclosed. (Form PTO/SB/06)

SIGNATURE OF Applicant or Assignee of Record

Signature

Date

Name

Telephone

Title and Company

Corporate Lawyer, X-FAB Semiconductor Foundries AG

NOTE: Signatures of all the inventors or assignees of record of the entire interest or their representative(s) are required. Submit multiple forms if more than one signature is required, see below.

☐ \*Total of \_\_\_\_\_ forms are submitted.

This collection of information is required by 37 CFR 1.31, 1.32 and 1.33. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.11 and 1.14. This collection is estimated to take 3 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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